-500mA / -12V Low VCE (sat) Digital transistors (with built-in resistors) DTB543XE / DTB543XM

Applications

Inverter, Interface, Driver

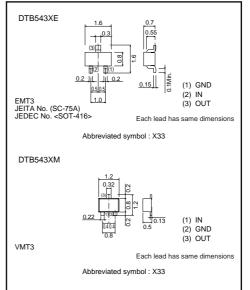
Feature

- 1) VCE (sat) is lower than conventional products.
- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 3) The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- Only the on / off conditions need to be set for operation, making the device design easy.

Structure

PNP epitaxial plannar silicon transistor (Resistor built-in type)

External dimensions (Unit : mm)



Packaging specifications

Code

Part No. DTB543XE

DTB543XM

Basic ordering

unit (pieces)

Package Packaging type

Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Farameter	Symbol	DTB543XE DTB543XM	
Supply voltage	Vcc	-12	V
Input voltage	Vin	-12 to +7	V
Collector current *1	IC (max)	-500	mA
Power dissipation *2	PD	150	mW
Junction temperature	Tj	150	ΰ
Storage temperature	Tstg	-55 to +150	r

*1 Characteristics of built-in transistor.*2 Each terminal mounted on a recommended land.

●Electrical characteristics (Ta=25°C)								
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions		
Input voltage	VI(off)	-	-	-0.3	V	Vcc=-5V, Io=-100µA		
	VI(on)	-2.5	-	-		Vo=-0.3V, Io=-20mA		
Output voltage	VO(on)	-	-70	-300	mV	lo/l=-100mA / -5mA		
Input current	lı I	-	-	-1.4	mA	VI= -5V		
Output current	IO(off)	-	_	-500	nA	Vcc=-12V, VI=0V		
DC current gain	Gi	140	-	-	-	Vo=-2V, Io=-100mA		
Transition frequency *	f⊤	-	260	-	MHz	Vce=-10V, Ie=5mA, f=100MHz		
Input resistance	R1	3.29	4.7	6.11	kΩ	_		
Resistance ratio	R2/R1	1.7	2.1	2.6	-	_		
* Characteristics of built-in transi	stor							

Equivalent circuit

EMT3

Taping

ТΙ

3000

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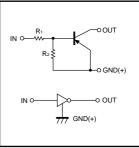
VMT3

Taping

T2L

8000

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 $R_1=4.7k\Omega / R_2=10k\Omega$

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